

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A pixel interpolation circuit for generating interpolation pixel data which interpolates an input image based on pixel data composing the input image, the pixel interpolation circuit comprising:

a plurality of interpolation circuits, each interpolation circuit independently calculating, using a different interpolation method interpolation candidate data of the same unknown interpolation pixel for each unknown interpolation pixel based on calculations performed on and test interpolation data of a plurality of normal pixels neighboring the interpolation pixel, where each interpolation circuit uses a different interpolation method, wherein said test interpolation data is calculated for each of said normal pixels on the assumption that said normal pixels are lost using said different interpolation method and sending each interpolation candidate data from each interpolation circuit for each unknown interpolation data to an output circuit is lost;

a determining circuit for selecting one of the interpolation circuits based on a difference between the test interpolation data and actual pixel data of said plurality of normal pixels for each unknown interpolation pixel and providing a selection signal to said output circuit; and

said an output circuit for outputting the interpolation candidate data calculated by the selected interpolation circuit as the interpolation pixel data according to the selection signal.
2. (Original) A pixel interpolation circuit according to claim 1, wherein the determining circuit calculates a evaluation data for each of the interpolation circuits, by summing up the absolute values of the difference between the test interpolation data and the actual pixel data, and selects one of the interpolation circuits based on the evaluation data.
3. (Original) A pixel interpolation circuit according to claim 1, wherein the determining circuit calculates binarized or ternarized values of the difference between the test interpolation data and the actual pixel data.

4. (Currently Amended) A pixel interpolation method for generating interpolation pixel data which interpolates an input image based on pixel data composing the input image, the pixel interpolation method comprising:

calculating using a plurality of interpolation circuits, interpolation candidate data of an unknown interpolation pixel and test interpolation data of a plurality of normal pixels neighboring the unknown interpolation pixel independently by each of said plurality of interpolation circuits ~~based on test interpolation data of a plurality of normal pixels neighboring the unknown interpolation pixel~~, where each of said plurality of interpolation circuits use a different interpolation method, wherein said test interpolation data is calculated for each of said normal pixels on the assumption that said normal pixels ~~are lost using said different interpolation method and sending each interpolation candidate data from each interpolation circuit for each unknown interpolation data to an output circuit is lost~~;

selecting one of the interpolation methods based on a difference between the test interpolation data and actual pixel data of said plurality of normal pixels for each unknown interpolation pixel ~~and providing a selection signal to said output circuit~~; and
outputting the interpolation candidate data calculated by the selected interpolation method as the interpolation pixel data ~~according to the selection signal~~.

5. (Original) A pixel interpolation method according to claim 4 further comprising,

calculating a evaluation data for each of the interpolation methods, by summing up the absolute values of the difference between the test interpolation data and the actual pixel data, wherein the interpolation method is selected based on the evaluation data.

6. (Original) A pixel interpolation circuit according to claim 4 further comprising,

calculating binarized or ternarized values of the difference between the test interpolation data and the actual pixel data.

7. (Original) An image scanner comprising a pixel interpolation circuit according to claims 1.